

Description

The LSI Computer Systems LFLS7366R-S is a 32-bit CMOS counter with a direct interface to quadrature signals from incremental encoders. It also interfaces with the index signals from incremental encoders to perform a variety of marker functions. A 4-wire SPI/MICROWIRE bus is used for communication with a host microprocessor.

The counter can be configured to operate as 1, 2, 3 or 4-byte counter and can count in a variety of modes such as quadrature, free-running, range limit and modulo-N. Hardware I/Os are provided for event driven operations, such as processor interrupt and index related functions.

[View the Manufacturer's Datasheet](#)



Features

- ▶ Operating voltage: 3V to 5.5V (VDD - VSS)
- ▶ 5V count frequency: 40MHz
- ▶ 3V count frequency: 20MHz
- ▶ 32-bit programmable counter
- ▶ 32-bit data register and comparator
- ▶ 32-bit output register
- ▶ 14-pin SOIC package

Base Pricing

Quantity	Price
1	\$5.60
10	\$4.75
50	\$4.00
100	\$3.50