

Description

The LSI Computer Systems LFLS7183 allow incremental encoders to drive standard up/down counters. Connect the encoder quadrature outputs to the A & B inputs. The LFLS7183 outputs can connect directly to the up and down clock inputs of counters such as 74193 or 40193. The LFLS7183 is an improved version of the LFLS7083 and should be considered first for all new product designs. The primary differences between the old and new LS chips are the addition of a x2 resolution multiplication, power supply operating range and improved output pulse timing characteristics.

Please note: Rbias values for output pulse width timing are not the same as the LFLS7083.

[View the Manufacturer's Datasheet](#)



Features

- x1, x2 or x4 resolution multiplication
- TTL and CMOS compatible
- Low current (micro-amps)
- 8-pin DIP package
- No external clocks required
- Drive standard Up/Down counters
- Monolithic CMOS
- Operates from 3V to 5V power supply

Base Pricing

Quantity	Price
1	\$3.20
10	\$2.95
50	\$2.70
100	\$2.60