LFLS7184-S Encoder to Counter Interface Chip

Description

The LSI Computer Systems LFLS7184-S allow incremental encoders to drive standard up/down counters. Connect the encoder quadrature outputs to the A & B inputs. The LFLS7184-S outputs can connect directly to the Clock and Up/Down inputs of counters such as 4516 or 74169.

The LFLS7184-S is an improved version of the LFLS7084-S and should be considered first for all new product designs. The primary differences between the old and new LS chips are the addition of a x2 resolution multiplication, power supply operating range and improved output pulse timing characteristics.

Please note: Rbias values for output pulse width timing are not the same as the LFLS7084-S.

View the Manufacturer's Datasheet

Features

- x1, x2 or x4 resolution multiplication
- TTL and CMOS compatible
- Low current (micro-amps)
- 8-pin SOIC package
- No external clocks required
- Drive standard Up/Down counters
- Monolithic CMOS
- Operates from 3V to 5V power supply

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